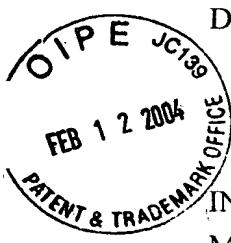


Receipt



DOCKET NO.: 240416US0DIV/bsg

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF:  
Masakazu KANECHIKA, et al.

GROUP: 2812

SERIAL NO: 10/618,085

ATTENTION:  
FILING RECEIPT CORRECTIONS

FILED: July 14, 2003

FOR: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE AND  
SEMICONDUCTOR DEVICE

**REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT**

Office of Initial Patent Examination  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

The Patent Office is requested to provide a corrected Official Filing Receipt for the attached. No fees are required. If you have any questions, please do not hesitate to contact us.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

A handwritten signature of Norman F. Oblon.

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/618,085	07/14/2003	2812	900	240416US0DIV	51	20	2

22850  
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 ALEXANDRIA, VA 22314

CONFIRMATION NO. 3653

UPDATED FILING RECEIPT



\*OC000000011675635\*

Date Mailed: 01/14/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

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 OBLON, SPIVAK, McCLELLAND  
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 Due Date(s): \_\_\_\_\_

**Assignment For Published Patent Application**

Kabushiki Kaisha Toyota Chuo Kenkyusho, Aichi-gun, JAPAN;

**Domestic Priority data as claimed by applicant**

This application is a DIV of 09/420,524 10/18/1999 ABN

**Foreign Applications**

JAPAN 10-313976 10/16/1998  
 JAPAN 11-092855 03/31/1999  
 JAPAN 11-172024 06/18/1999  
 JAPAN 11-242883 08/30/1999

**If Required, Foreign Filing License Granted:** 10/10/2003

**Projected Publication Date:** 04/22/2004

**Non-Publication Request:** No

**Early Publication Request:** No

**Title**

Method of manufacturing semiconductor device and semiconductor device

**Preliminary Class**

438

**PLEASE NOTE THAT THE APPLICANTS' CITIES ARE INCORRECT. THEY SHOULD READ AS FOLLOWS:**

MASAKAZU KANECHIKA, AICHI, JAPAN  
 KENJI NAKASHIMA, AICHI, JAPAN  
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